WATTELH

HTN9G49S007P 7W, 4800 - 4900 MHz LDMOS Amplifier

Product datasheet

Description

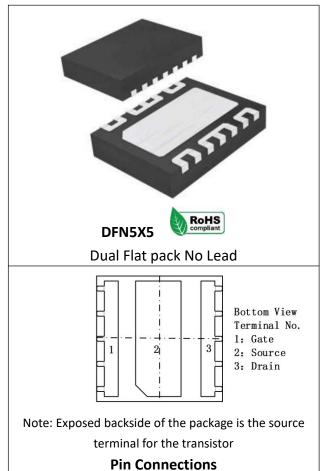
The HTN9G49S007P is an unmatched discrete LDMOS Power Amplifier with 7W saturated output power covering frequency range from 4800 - 4900 MHz.

Features

- Operating Frequency Range: 4800 4900 MHz
- Operating Drain Voltage: +24V
- Saturation Output Power: 7W
- Power Average: 0.76W
- Excellent thermal stability due to low thermal resistance package
- Enhanced robustness design without device degradation
- Internally integrated enhanced ESD design

Applications

- CDMA
- W-CDMA
- GSM EDGE
- MC-GSM
- TDD/FDD LTE
- WiMAX



Ordering Information

Part Number	Description
HTN9G49S007P	Reel Package
HTN9G49S007PEVB	4800 - 4900 MHz EVB

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Typical Performance

RF Characteristics (CW)

Freq (MHz)	Gain (dB)	P1dB (dBm)	Eff (%)@ P1dB (dBm)	P3dB (dBm)	Eff (%)@ P3dB (dBm)
4800	15.18	36.97	41.90	38.01	44.51
4850	15.46	37.02	43.59	38.04	46.09
4900	15.75	36.87	43.92	37.93	46.48

Test conditions unless otherwise noted: 25 °C, VDD = +24Vdc, IDQ= 60mA test on WATECH Application Board

RF Characteristics (Pulsed CW)

Freq (MHz)	Gain (dB)	P1dB (dBm)	Eff (%)@ P1dB (dBm)	P3dB (dBm)	Eff (%)@ P3dB (dBm)
4800	15.28	37.44	44.05	37.47	46.78
4850	15.52	37.50	45.99	38.50	48.55
4900	15.87	37.37	46.74	38.38	49.39

Test conditions unless otherwise noted: 25 °C, VDD = +24Vdc, IDQ=60mA, PW = 100us, DC= 10% test on WATECH Application Board

Absolute Maximum Ratings

Parameter	Range/Value	Unit
Drain voltage (VDSS)	-0.5, +65	V
Gate voltage (VGs)	-5 to +10	V
Operation voltage (VDD)	+0 to +24	V
Storage Temperature (Tstg)	-55 to +150	°C
CasecTemperature (Tc)	-40 to +150	°C
Junction Temperature (TJ)	-40 to +225	°C

Electrical Specification

DC Characteristics

Parameter	Conditions	Min	Тур	Max	Unit
Breakdown Voltage V(BR)DSS	Vgs=0V, Ids=7.2uA	65	-	-	V
Gate-Source Threshold Voltage V _{GS(th)}	Vds=Vgs, Ids=7.2uA	-	1.5	-	V
Drain Leakage Current IDSS	Vgs=0V, Vds=65V	-	-	1.0	uA
Gate Leakage Current Isss	Vgs=5V, Vds=0V	-	-	100	nA



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Load Mismatch Test

Condition	Test Result	
VSWR=10:1, at all Phase Angles, VDD = +24Vdc, IDQ= 60mA,	No Dovico	
CW signal Pout = 38 dBm (3dB input Overdrive from P3dB) @4850 MHz test	No Device	
on WATECH Application Board	Degradation	

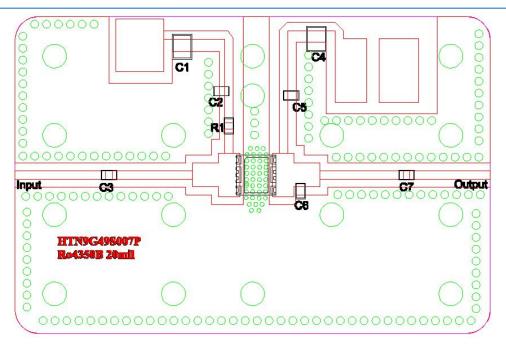
Thermal Information

Parameter	Condition	Value (Typ)	Unit
Thermal Resistance	TCASE= 50°C, CW signal 7W	2.0	°C /W
Junction to Case (Rтн)	TCASE - 50 C, CW Signal 7 W	3.0	C / W

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HTN9G49S007P 4800 - 4900 MHz Reference Design



EVB Layout

Bill of Materials (BoM) - HTN9G49S007P 4800 - 4900 MHz Reference Design

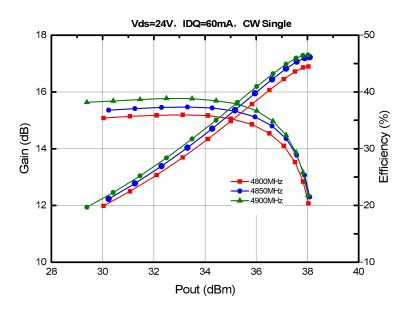
Reference	Value	Description	Manufacturer	P/N
Q1	-	20W, 4800 - 4900 MHz LDMOS PA	Watech	HTN9G49S007P
C1, C4	4u7F	MLCC	Murata	GCM32DC72A475KE02L
C2, C7	3p9F	MLCC	Murata	GQM2195G2E3R9BB12
С3	10pF	MLCC	Murata	GQM2195C2E100JB12
C5	6p8F	MLCC	Murata	GQM2195G2E6R8BB12
C6	0p6F	MLCC	Murata	GQM2195G2ER60BB12
R1	10Ω	Thick Film Resistor	-	0603
РСВ	Rogers4350B (er = 3.66), 20 mil (0.508 mm), 35 μm (1oz)			



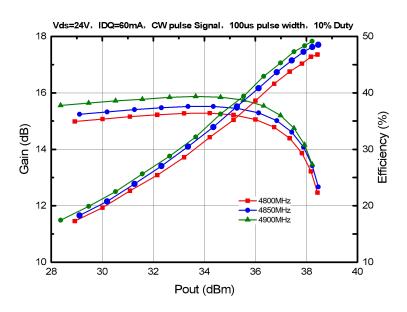
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Performance Plots

4800 - 4900 MHz Reference Design

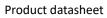






Pulsed CW, Gain and Efficiency vs Pout

Test conditions unless otherwise noted: 25 °C, VDD = +28Vdc, IDQ= 60mA, PW = 100us, DC= 10% test on WATECH Application Board



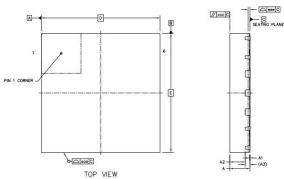
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Package Marking and Dimensions

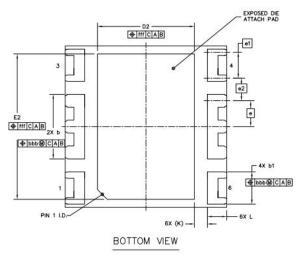


- Line1 (fixed): Device name in W/O
- Line2 (unfixed): Take the last 8 digits of Marking Lot No in W/O (Sample: E596-20140001, just take "20140001")
- Line3 (unfixed): Date Code + JY This Marking SPEC only stipulates the content of Marking. For marking requirements such as font and size, please refer to the latest version of "Watech Product Printing Specification"









		SYMBOL	MIN	NOM	MAX
TOTAL THICKNESS		Α.	0.8	0.85	0.9
STAND OFF		A1	0	0.02	0.05
MOLD THICKNESS		A2		0.65	
L/F THICKNESS		A3		0.203 REF	
		ь	1.95	2	2.05
LEAD WIDTH		ь1	0.95	1	1.05
BODY SIZE	×	D		5 BSC	
	Y	E		5 BSC	
	44	e	0.8 BSC		
LEAD PITCH		e1	0.8 BSC		
		e2	0.7 BSC		
EP SIZE	×	D2	2.9	3	3.1
EF SIZE	Y	E2	4.4	4.5	4.6
LEAD LENGTH		L	0.5	0.6	0.7
LEAD TIP TO EXPOSE	D PAD EDGE	к	0.4 REF		
PACKAGE EDGE TOLE	RANCE	000	0.1		
MOLD FLATNESS		ccc	0.1		
COPLANARITY		ece	0.08		
LEAD OFFSET		bbb	0.1		
EXPOSED PAD OFFSET		fff	0.1		

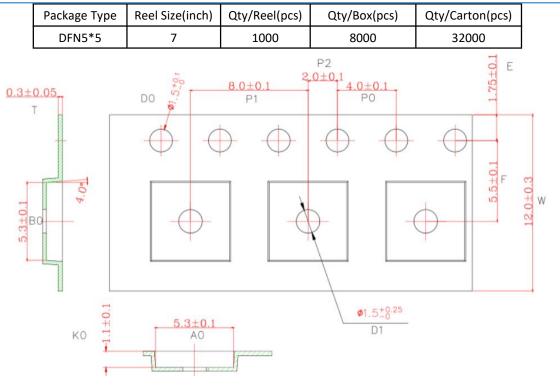
Package Dimensions

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Tape and Reel Information



Tape & Reel Packaging Descriptions

Handling Precautions

Parameter	Grade
Moisture Sensitivity Level MSL	3

Parameter	Rating	Standard	
ESD – Human Body Model (HBM)	Class 1B	JESD22-A114	ATTENTION OBSERVE PRECAUTIONS FOR HANDLING
ESD–Human Body Model (MM)	Class A	EIA/JESD22-A115	ELECTROSTATIC SENSITIVE DEVICES
ESD – Charged Device Model (CDM)	Class III	JESD22-C101	

RoHS Compliance

This product is compliant with the 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment), as amended by Directive 2015/863/EU.



Product datasheet

Datasheet Status

Document status	Product status	Definition
Objective Datasheet	Design simulation	Product objective specification
Preliminary Datasheet	Customer sample	Engineering samples and first test results
Product Datasheet	Mass production	Final product specification

Abbreviations

Acronym	Definition	
LDMOS	Laterally-Diffused Metal-Oxide Semiconductor	
CW	Continuous Waveform	

Revision history

Document ID	Datasheet Status	Release Date	Revision Version
Rev 1.0	Preliminary	Dec. 2021	Initial
Rev 1.1	Preliminary	April 2022	Update logo and name of Watech
Rev 1.2	Product	Oct. 2022	Update test data
Day 1.2	Product	March 2023	New format based on English version
Rev 1.3			datasheet
Rev 1.4	Product	Jan.2024	Update Package size table and TBD
Rev 1.4			information

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Contact Information

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Product datasheet

For the latest specifications, additional product information, worldwide sales and distribution locations and information about WATECH:

- Web: <u>www.watechelectronics.com</u>
- Email: <u>MKT@huatai-elec.com</u>

For technical questions and application information:

• Email: <u>MKT@huatai-elec.com</u>

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