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H8G2022M10P 10W, 2.110 - 2.200 GHz LDMOS MMIC Amplifier

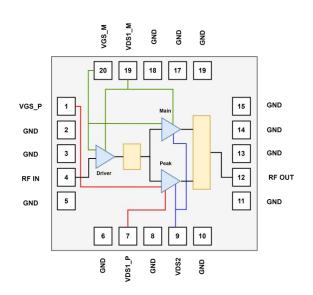
Product datasheet

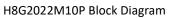
Description

The H8G2022M10P is a LDMOS MMIC Integrated Asymmetrical Doherty based on 2-Stage with 10W saturated output power covering frequency range from 2.110 - 2.200 GHz.

The amplifier is 50 Ω Input/Output matched with a small compact footprint 7x7 mm which makes it ideal for integration.

Block Diagram





Applications

- 3GPP 5G NR FR1 n1/65/66 and 4G-LTE band B1/4/65/66
- Power Amplifier for Small Cells
- Driver Amplifier for Micro and Macro Base Stations
- Active Antenna Array for 5G mMIMO
- Repeaters/DAS
- Mobile Infrastructure



20 Pin LGA 7x7 mm Plastic Package

Features

- Operating Frequency Range: 2.110 2.200
 GHz
- Operating Drain Voltage: +28V
- Saturation Output Power: 10W
- Power Average: 1.25W
- 50 Ω Input/Output matched
- Integrated Input Divider
- Integrated Output Combiner
- Integrated Asymmetrical Doherty Final Stage
- High Efficiency: 43.1%@1.880GHz, WCDMA
- High Gain: 27.7dB@1.880GHz, WCDMA
- Small footprint package: LGA 7x7 mm

Ordering Information

Part Number	Description
H8G2022M10P	Reel Package
H8G2022M10PEVB	2.110 - 2.200 GHz EVB



Product datasheet

Typical Performance

RF Characteristics (Pulsed CW)

Freq (GHz)	P3dB (dBm)	Gain (dB)	Eff (%)	IRL (dB)
2.110	39.9	27.8	44.1	-37.9
2.200	39.7	27.4	42.7	-21.9

Test conditions unless otherwise noted: 25 °C, VDD = +28Vdc, IDQ = 26mA, Vgsp = Vgsm-0.46V, Pulse Width = 100us, Duty Cycle = 10% test on WATECH Application Board

RF Characteristics (WCDMA)

Freq (GHz)	Gain (dB)	Eff (%)	IRL (dB)	ACPR* @5MHz (dBc)	ACPR* @10MHz (dBc)
2.110	27.2	42.9	34.2	-29.7	-42.8
2.200	26.9	41.8	21.7	-32.2	-44.4

Test conditions unless otherwise noted: 25 °C, VDD=+28Vdc, IDQ = 26mA, Vgsp = Vgsm-0.46V, PAVG = 31 dBm 1C-WCDMA 5MHz Signal, 7.2 dB PAR @ 0.01% CCDF test on WATECH Application Board *Uncorrected DPD

Absolute Maximum Ratings

Parameter	Range/Value	Unit
Drain voltage (VDSS)	-0.5 to +65	V
Gate voltage (VGs)	-5 to +10	V
Drain voltage (VDD)	0 to +28	V
Storage Temperature (Tstg)	-55 to +150	°C
Case Temperature (Tc)	-40 to +125	°C
Junction Temperature (T _J)	-40 to +175	°C



Product datasheet

Electrical Specification

DC Characteristics

Parameter	Conditions	Min	Тур	Max	Unit
Breakdown Voltage V(BR)DSS	Vgs=0V, Ids=100uA	65	-	-	V
Gate-Source Threshold Voltage V _{GS(th)}	Vgs=Vds, Ids=5.2uA	1.2	-	1.8	V
Drain Leakage Current IDSS	Vgs=0V, Vds=28V	-	-	0.5	uA
Gate Leakage Current Igss	Vgs=5V, Vds=0V	-	-	0.05	uA

RF Characteristics (Pulsed CW)

Parameter	Freq (GHz)	Min	Тур.	Max	Unit
P3dB	2.110	39.4	39.9	-	dBm

Test conditions unless otherwise noted: 25 °C, VDD = +28Vdc, IDQ = 26mA, Vgsp = Vgsm-0.46V, Pulse Width = 100us, Duty Cycle = 10% test on WATECH Production Board

RF Characteristics (WCDMA)

Parameter	Conditions	Min	Тур.	Max	Unit
Frequency	2.110				GHz
Gain	PAVG = 31 dBm	25.5	27.5	30	dB
Eff	PAVG = 31 dBm	39.5	43	-	%
IRL	PAVG = 31 dBm	10	15	-	dB
ACPR@5MHz*	PAVG = 31 dBm	-	-29.5	-27.5	dBc

Test conditions unless otherwise noted: 25 °C, VDD=+28Vdc, IDQ = 26mA, Vgsp = Vgsm-0.46V, 1C-WCDMA 5MHz Signal, 7.2 dB PAR @ 0.01% CCDF test on WATECH Production Board

*Uncorrected DPD

Load Mismatch Test

Condition	Test Result
VSWR=10:1, at all Phase Angles, VDD=+28Vdc, IDQ = 28 mA,	No Device
Vgsp=Vgsm-0.46V, PAVG = 34 dBm, Frequency 2.155 GHz, test on WATECH	
Application Board	Degradation

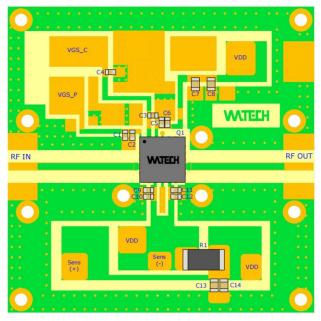
Thermal Information

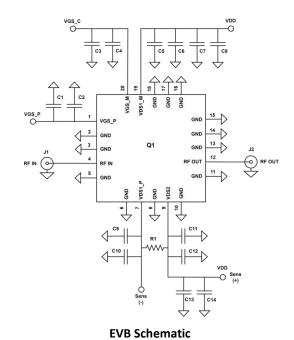
Parameter	Condition	Value (Typ)	Unit
Thermal Resistance	Tcase= 90°C, 1C-WCDMA 5MHz	11	°C /W
Junction to Case (RTH)	Signal, 7.2 dB PAR, PAVG = 31 dBm		0,11



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H8G2022M10P 2.110 - 2.200 GHz Reference Design (47 x47 mm)





EVB Layout

Bill of Materials (BoM) - H8G2022M10P

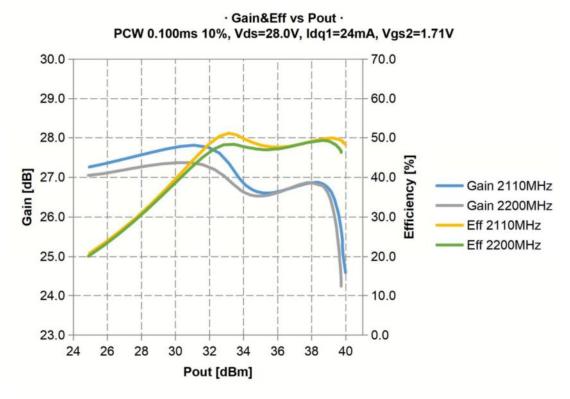
2.110 - 2.200 GHz Reference Design

Reference	Value	Description	Manufacturer	P/N	
01		10W, 2.110 - 2.200 GHz	Watach	H8G2022M10P	
Q1	-	LDMOS MMIC PA	Watech	HoG2022IVI10P	
C7,C8,	1uF ±10%,	Multi-Layer Ceramic	Murata	GRM219R7YA105KA12	
C13,C14	0805	Capacitor	IVIUIdla	GRIVIZISK/ MIUSKAIZ	
C1-C6,	1uF ±10%,	Multi-Layer Ceramic	Murata	GCM188R71E105KA64D	
C9 - C12	0603	Capacitor	Wurata	UCIVITOSIA ILIUSIA04D	
R1	$100 \text{m}\Omega/1 \text{W}$, High Procision	High-Precision Resistor	Vishay	Y44870R10000B0R	
	0.1%		VISITAY	1448701100000001	
	 Rogers 4350B, er = 3.66; Thickness= 20 mil (0.508 mm); Thickness copped 				
PCB plating = 35 μm (1oz)					
	 Soldered on a 47x47x10 mm Copper Base-Plate 				



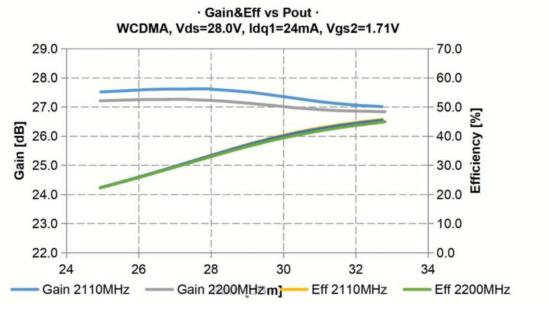
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Performance Plots



Pulsed CW, Gain and Efficiency vs Pout

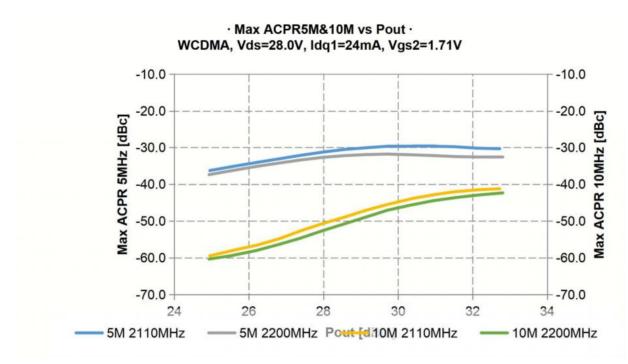
Test conditions unless otherwise noted: 25 °C, VDD = +28Vdc, IDQ = 26mA, Vgsp = Vgsm-0.46V, Pulse Width = 100 us, Duty Cycle = 10% test on WATECH Application Board



WCDMA, Gain and Efficiency vs Pout

Test conditions unless otherwise noted: 25 °C, VDD=+28Vdc, IDQ = 26mA, Vgsp = Vgsm-0.46V, 1C-WCDMA 5MHz Signal, 7.2 dB PAR @ 0.01% CCDF test on WATECH Application Board

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WCDMA, ACPR_5MHz, ACPR_10MHzvs Pout

Test conditions unless otherwise noted: 25 °C, VDD=+28Vdc, IDQ = 26mA, Vgsp = Vgsm-0.46V, 1C-WCDMA 5MHz Signal, 7.2 dB PAR @ 0.01% CCDF test on WATECH Application Board

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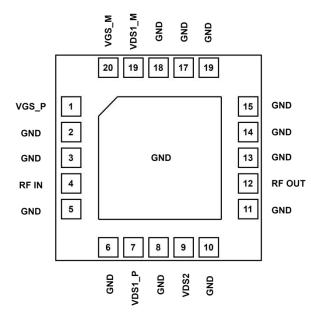
H8G2022M10P



10W, 2.110 - 2.200 GHz LDMOS MMIC Amplifier

Product datasheet

Pin Configuration and Description



GND	Ground
GND	Ground
GND	Ground
GND	Ground
	Drain-Source
VDS1_M	Voltage Main
	Driver
	Gate-Source
	Voltage Main
	GND GND GND

Pinout Device Configuration

Pin Number	Label	Description
1	VGS P	Gate-Source
L	VG2_P	Voltage Peak
2	GND	Ground
3	GND	Ground
4	RFIN	RF Input
5	GND	Ground
6	GND	Ground
		Drain-Source
7	VDS1_P	Voltage Peak
		Driver
8	GND	Ground
		Drain-Source
9	VDS2	Voltage Final
		Stage
10	GND	Ground
11	GND	Ground
12	RFOUT	RF Output
13	GND	Ground
14	GND	Ground

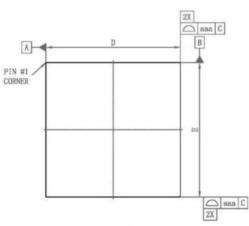
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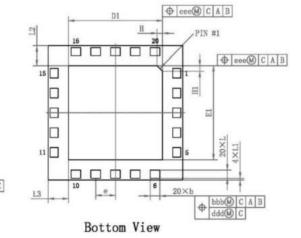
Package Marking and Dimensions

- LIne1 (fixed): Device name in W/O
- Line2 (unfixed): Marking Lot No in W/O (Sample: E596-20140001)
- Line3 (unfixed): Date Code + JY

• This Marking SPEC only stipulates the content of Marking. For marking requirements such as font and size, please refer to the latest version of "Watech Product Printing Specification"









Side View

C

SEATING PLANE

symbol	Dimension in mm			Dimension in inch		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.760	0.860	0.960	0.030	0.034	0.038
c	0.150	0.180	0.210	0.006	0.007	0.008
D	6.900	7.000	7.100	0.272	0.276	0.280
E	6.900	7.000	7.100	0.272	0.276	0.280
DI	4.800	4.900	5.000	0.189	0.193	0. 197
E1	4.800	4.900	5.000	0.189	0.193	0. 197
H		0.286		-	0.011	-
H1		0.286		-	0.011	-
L	0.370	0.420	0.470	0.015	0.017	0.019
LI	0.025	0.100	0.175	0.001	0.004	0.007
12	0.975	1.050	1.125	0.038	0.041	0.044
13	0.975	1.050	1.125	0.038	0.041	0.044
e		1.030			0.041	-
b	0.450	0.500	0.550	0.018	0.020	0.022
888	0.150		0.006			
bbb	0.150		0.006			
ccc	0.100		0.004			
ddd	0.080		0.003			
cec	0.150		0.005			

Marking

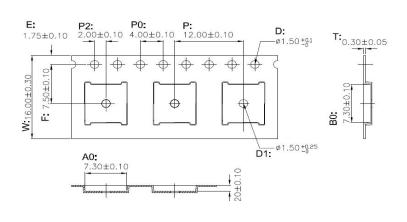
Package Dimensions

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H8G2022M10P 10W, 2.110 - 2.200 GHz LDMOS MMIC Amplifier

Product datasheet

Tape and Reel Information



Notes:

1. Carrier tape color: BLACK.

2. Carrier material :PS (Polystyrene).

3. ESD surface resistivity < 1× 1011 Ω /square per EJA, JEDEC TNR specification.

4. Heat deflection temperature for Tape& Reel material: 62°C

- 5. Vicat softening temperature (10N) for Tape & Reel material: 95°C
- 6. Dimension is millimeter.



Tape & Reel Packaging Descriptions

Handling Precautions

Parameter	Grade
Moisture Sensitivity Level MSL	3

Parameter	Rating	Standard	
ESD – Human Body Model (HBM)	Class 1B	JESD22-A114	
ESD – Human Body Model (MM)	Class A	EIA/JESD22-A115	FOR HANDLING ELECTROSTATIC SENSITIVE DEVICES
ESD – Charged Device Model (CDM)	Class III	JESD22-C101	

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Product datasheet

RoHS Compliance

This product is compliant with the 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment), as amended by Directive 2015/863/EU.

Datasheet Status

Document status	Product status	Definition
Objective Datasheet	Design simulation	Product objective specification
Preliminary Datasheet	Customer sample	Engineering samples and first test results
Product Datasheet	Mass production	Final product specification

Abbreviations

Acronym	Definition
LDMOS	Laterally-Diffused Metal-Oxide Semiconductor
CW	Continuous Waveform
VSWR	Voltage Standing Wave Ratio

Revision history

Document ID	Datasheet Status	Release Date	Revision Version	
Rev 3.2	Product	May 2020	Product release	
Rev 3.3	Product	March 2023	New format based on English	
			version datasheet	

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Product datasheet

Contact Information

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